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(Use as many sheets as necessary)

Sheet	1	of	1

Complete if Known	
Application Number	10/683,859
Filing Date	10/10/2003
First Named Inventor	Brent Kelley
Art Unit	2112
Examiner Name	STIGLIC, Ryan M.
Attorney Docket Number	TT5565

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	1-A	"Swift Interconnects Ripple the Multiprocessing Waters," Cost Journal, Online, XP002342675, January 2003, pp. 60-62	
RS	2-A	"The AMD Opteron Processor for Multiprocessor Servers," Chetana N. Keltcher et al., IEEE, 2003, pp. 66-76	
	3-A		
	4-A		
	5-A		
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Examiner Signature	/Ryan Stiglic/	Date Considered	09/22/2006

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